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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No
Filing Date
Inventor Luan C. Tran
Assignee Micron Technology, Inc.
Assignee
Group Art Unit
Examiner Laura Schillinger
Atternavia Docket No.
Title: Semiconductor Processing Methods Of Forming Transisions, Semiconductor
Processing Methods Of Forming Dynamic Random Access Memory Circuitry, and
Flocessing Methods Critically Systems Commission
Related Integrated Circuitry

Mail Stop Appeals - Patents Commissioner for Patents P.O. Box 1450 Alexandria VA 22313-1450

## **CERTIFICATE OF FACSIMILE TRANSMISSION UNDER 37 CFR 1.8**

I hereby certify that the following papers are being facsimile transmitted to the Patent and Trademark Office at (571) 273-8300 on the date shown below:

- RCE Transmittal Form (PTO/SB/30)
- 2. Preliminary Amendment
- 3. Fee Transmittal (PTO/SB/17) in duplicate
- 4. Declaration of Luan C. Tran including Exhibit A

Dated: 2/21/2004

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NUMBER OF PAGES IN FACSIMILE: 26

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